

**Features**

- Floating point Digital Processor
- Flexible configurations
- Low power & small form factor
- Large FIR & IIR filter banks

**Hardware**

- Analog Devices ADSP21369
- ESS 9023 DAC
- 32bit Floating point processing
- Stereo digital inputs (AES-EBU/SPDIF/Optical)
- Stereo balanced analog output (XLR connector)
- ASRC for 20 to 216kHz input
- Front panel volume control
- IR control with learning feature

**Software Control**

- Real time live control
- Win & Mac compatible
- Firmware upgradeable
- 4 preset memory

**Power**

- Single external 5VDC supply
- Low power (3W)

**Applications**

- Room correction using FIR filters
- Advanced filtering applications
- System equalization
- Mobile Audio

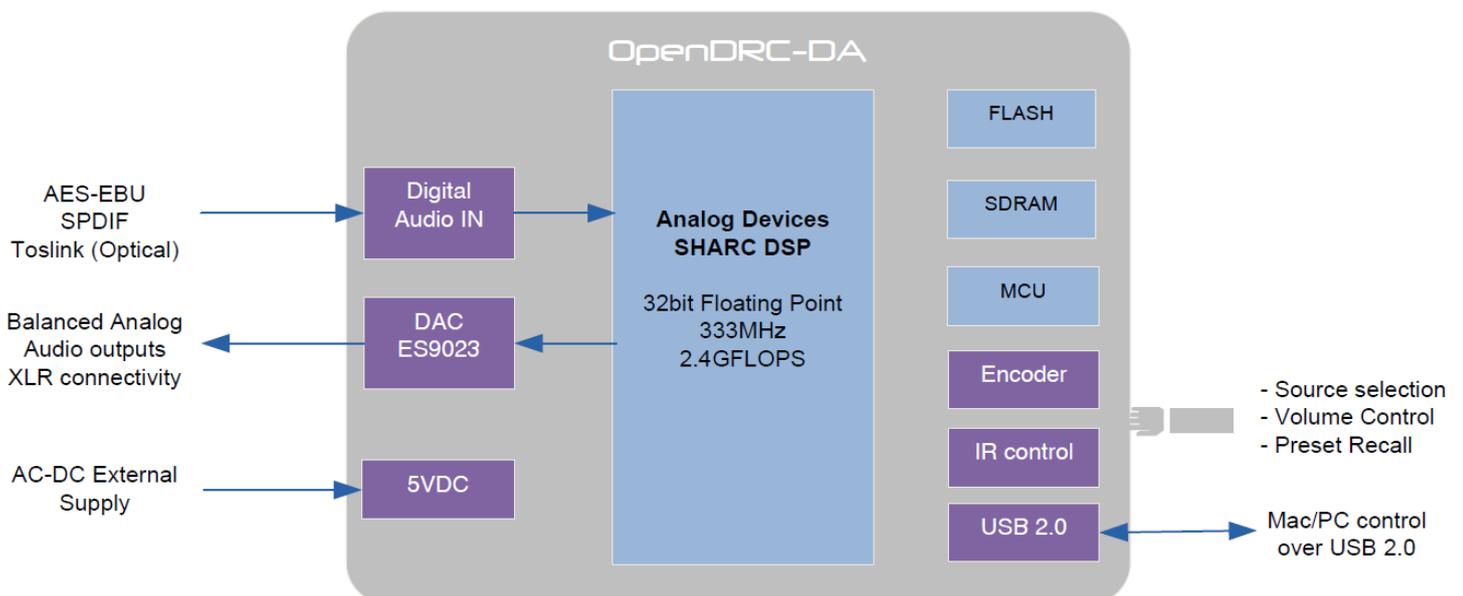
Introducing the OpenDRC platform, a range of cost effective digital audio processor with floating point capabilities. Powered by Analog Devices Sharc processors, the OpenDRC engine handles complex audio filtering such as room correction, FIR crossover filtering, reverb engines...Thanks to the flexibility of the platform and its high performance, OpenDRC supports a wide variety of applications.

The OpenDRC-DA (Digital to Analog) carries the most common stereo digital audio formats (AES-EBU, SPDIF and Toslink) on the input side. With its on-board Asynchronous Sample Rate Converter (ASRC), digital audio is converted to the proper rate. On the output side, the praised ES9023 DAC from ESS Tech provides outstanding analog audio performance. The learning remote feature and/or rotary encoder will allow control of your source, active preset or master volume without the need of any PC once the unit is configured.

Last but not least, the OpenDRC-DA follows the footsteps of our proven miniDSP concept: "One hardware, many plug-ins". An easy to use platform that received praises for its simplicity of use. By setting some strategic partnership with 3rd party software developers, the OpenDRC takes it one step further in harnessing DSP powers in audio applications. From advanced room correction to full featured linear phase crossover, the OpenDRC opens up a



SYSTEM DIAGRAM



## HARDWARE SPECIFICATIONS

Item	Description
Digital Signal Processor	32bit Floating point Analog Devices SHARC ADSP21369 / 333MHz
Control	Driverless USB 2.0 control interface for Windows/Mac OS x environments A computer is only required for the initial configuration.
Digital Audio inputs	Digital audio source selectable from IR remote or Front panel: - AES-EBU on Neutrik 3pin female XLR / Isolated with digital audio transformer - SPDIF on RCA connector / Isolated with digital audio transformer - Toslink on Optical connector The input signal is processed by a high quality onboard Asynchronous Sample Rate Converter for compatibility with most common sample rate (20-216kHz)
Analog Audio outputs	- Fully balanced audio on Balanced Neutrik XLR connector - ESS DAC 9023 with 112dB SNR - Max output: 2Vrms
Sample rate / Resolution	Resolution: 32bit Sample rate: Depends on selected plug-in. Please consult plug-in datasheet for more information on the operating sample rate of the DSP
Template FIR filter capabilities (Important note: FIR capabilities are controlled by the plug-in used and not the hardware itself).	Mono signal: FIR filter with up to 12228 taps @48kHz, 6144 @ 96kHz Stereo signal: FIR filter with up to 6144 taps/ch @48kHz Please consult the plug-in specs for more info.
FIR filter storage	FIR taps coefficients & DSP configuration automatically loaded at bootup
USB port	USB port type B for real time control and firmware upgrade
Power supply	5VDC single supply / 600mA @ 5V - 2.1 round plug
Dimensions (H x W x D) mm	52 x 180 x 200mm

## MECHANICAL SPECIFICATIONS

